

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2001-021636

(43)Date of publication of application : 26.01.2001

(51)Int.Cl.

G01S 5/14  
// G01C 21/00

(21)Application number : 11-193657

(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

(22)Date of filing : 07.07.1999

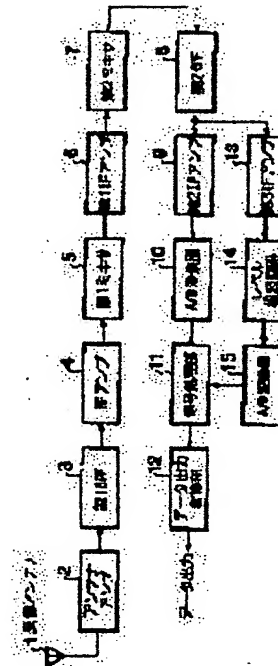
(72)Inventor : TAJIMA HIROSHI

## (54) RECEIVER FOR POSITIONING SYSTEM

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To obtain a receiver which detects whether interfering radio waves have been received or not so as to output a detection result.

**SOLUTION:** The level of a received signal is detected from the output signal of a second BPF 8 by an independent system which is composed of a third IE amplifier 13, a level detection circuit 14 and an A/D converter 15 separately from that of the detection of positioning data. When an output is dropped due to interfering radio waves, its drop is output from a signal processing part 11 together with the positioning data. Thereby, a host apparatus such as a navigation apparatus which uses the positioning data to be output can recognize that the interfering radio waves are contained, and it can confirm the reliability of its positioning result. When a reception level which is detected by the level detection circuit 14 exceeds a preset level, the output of the positioning result may be inhibited.



## LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office